T-576 P.08/29 F-243

Docket No. 96-108

PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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) Examiner: D. Langjahr
) '·
) Art Unit: 2752
)

For: System And Method For Assessing And Controlling The Operational Effectiveness

Of A Cache Memory In A Digital Data Processing System

Richard A. Jordan P. O. Box 81363 Wellesley Hills, MA 02481-0004 (781) 431-1357 September 16, 2001

Certificate of Mailing

I hereby certify that the following correspondence dated September 16, 2001, is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents And Trademarks, Washington, D.C. 20231 on September 16, 2001.

Respectfully submitted,

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Richard A. Jordan

Reg. No. 27,807

OFFICE OF PETITIONS

Honorable Commissioner of Patents and Trademarks Washington, D.C. 20231

Sir:

Information Disclosure Statement

-1-

Serial Number 08/833,410 Filing Date April 4, 1997 Information Disclosure Statement Mailing Date September 16, 2001

Docket No. 96-108

Pursuant To 37 C.F.R. §1.97, et seq., Applicant respectfully notifies the U. S. Patent And Trademark Office of the information identified on the accompanying PTO Form PTO-1449. A copy of each document is also enclosed.

Statement Under 37 C.F.R. §1.97(b)(1)

The enclosed Information Disclosure Statement is being filed within three months of the filing date of the above-identified national application or Request For Continued Examination Under 35 U. S. C. §114. Accordingly, it is believed that no fee is due for this Information Disclosure Statement.

Respectfully submitted, Rulard Johan

Richard A. Jordan Register. No. 27,807

Telephone Numbers:

Voice: (781) 431-1357 Fax: (781) 235-8326

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September 16, 2001

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FORM PTO-1449 (Rev. 2-32) (Substitute) U. S. Department of Commerce Patent And Trademark Office		ATTY. DOCKET NO. 96-108	5	SERIAL NO. 08/833,410				
		APPLICANT Eitan Bachmat						
	INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		FILING DATE GROUP					
			April 4, 1997	'	2752			
	(0.			DOCUMENTS				
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	AR	A. K. Bhide et al "A simple analysis of the LRU buffer policy and its relationship to buffer warm-up transient" (IEEE, 1993)						
	AS	D. Buck et al "An analytic s	study of caching	in computer systems" (Ac	ademic Pre	ss, 1996)	
	AT							
EXAMINER			DATE CONSIDERED					

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449[6-4])(substitute)